

Serial No. 16Y1011090029

Roll No. 160110900023

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE
1st Semester of ELECTRICAL ENGINEERING Diploma Examination
Held In December 2016

Name of the Candidate : GURPREET ✓
Father's Name : HARMESH LAL ✓
Mother's Name : PARAMJIT ✓
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

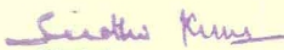
Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	English Language-I (120011)	64/100	44/50	108/150
02	Applied Maths-I (120012)	75/100	-	75/100
03	Applied Physics-I (120013)	53/100	38/50	91/150
04	Applied Chemistry-I (120014)	82/100	41/50	123/150
05	Basic of Information Technology (120016)	-	80/100	80/100
06	Engineering Drawing - I (120015)	50/100	20/25	70/125
07	General Workshop Practice-I (120017)	-	65/100	65/100
08				
09				
	Internal Assessment			288/375
	Grand Total			898/1250

Total(In Words) : EIGHT HUNDRED NINETY-EIGHT ✓

Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula


Principal

Dated: 17-02-2017


Deputy Secretary

Haryana State Board of Technical Education
Panchkula



Controller of Examination
Haryana State Board of Technical Education
Panchkula

Serial No. 17Y2011090004M06

Roll No. 160110900023

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE
2nd Semester of ELECTRICAL ENGINEERING Diploma Examination
Held In June 2017

Name of the Candidate : GURPREET
Father's Name : HARMESH LAL
Mother's Name : PARAMJIT
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	English Language - II (120021)	53/100	40/50	93/150
02	Applied Mathematics - II (120022)	49/100	-	49/100
03	Applied Physics -II (120023)	81/100	38/50	119/150
04	Applied Chemistry-II (120024)	54/100	40/50	94/150
05	Fundamental of Electrical Engineering (120926)	55/100	44/50	99/150
06	Engineering Drawing II (120025)	44/100	20/25	64/125
07	General Workshop Practice II (120027)	-	78/100	78/100
08				
09				
Internal Assessment				283/375
Grand Total				879/1300

Total(In Words) : EIGHT HUNDRED SEVENTY-NINE

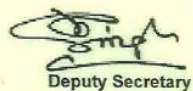
Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula


Principal

Dated: 30-06-2017


Deputy Secretary

Haryana State Board of Technical Education
Panchkula



Controller of Examination
Haryana State Board of Technical Education
Panchkula

Serial No. 18Y3011090016M06

Roll No. 160110900023

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE

**3rd Semester of ELECTRICAL ENGINEERING Diploma Examination
Held In June 2018**

Name of the Candidate : GURPREET
Father's Name : HARMESH LAL
Mother's Name : PARAMJIT
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Electrical and Electronics Engineering Materials (120931)	76/100	-	76/100
02	Electrical Measurements and Measuring Instruments (120932)	66/100	38/50	104/150
03	Electronics-I (120933)	70/100	40/50	110/150
04	Electrical Engineering Design and Drawing (120934)	51/100	41/50	92/150
05	Computer Programming and Applications (120935)	63/100	37/50	100/150
06	Electrical Workshop Practice	-	42/50	42/50
07				
08				
09				
Internal Assessment				274/375
Grand Total				798/1125

Total(In Words) : SEVEN HUNDRED NINETY-EIGHT

Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula

Principal

Dated: 25-06-2018

Deputy Secretary

Haryana State Board of Technical Education
Panchkula

Controller of Examination

Haryana State Board of Technical Education
Panchkula

Serial No. 18Y4011090012M06

Roll No. 160110900023

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE
4th Semester of ELECTRICAL ENGINEERING Diploma Examination
Held In June 2018

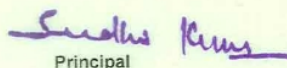
Name of the Candidate : GURPREET
Father's Name : HARMESH LAL
Mother's Name : PARAMJIT
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Electrical Machines-I (120941/030941)	68/100	39/50	107/150
02	Energy Sources and Management of Electrical Energy (120942)	59/100	-	59/100
03	Electronics-II (120943)	51/100	35/50	86/150
04	Electrical Engineering Design and Drawing - II (120944/030944)	49/100	38/50	87/150
05	Instrumentation (120946)	52/100	38/50	90/150
06	Estimating and Costing in Electrical Engineering (120945/030945)	56/100	-	56/100
07				
08				
09				
Internal Assessment				242/300
Grand Total				727/1100

Total(In Words) : SEVEN HUNDRED TWENTY-SEVEN
Result: PASS

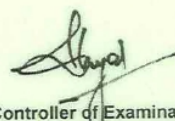
* Indicates that the candidate passed with grace marks.

Place: Panchkula


Principal

Dated: 25-06-2018


Deputy Secretary
Haryana State Board of Technical Education
Panchkula


Controller of Examination
Haryana State Board of Technical Education
Panchkula

Serial No. 18Y5011090008M12

Roll No. 160110900023

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE 5th Semester of ELECTRICAL ENGINEERING Diploma Examination Held In December 2018

Name of the Candidate : GURPREET
Father's Name : HARMESH LAL
Mother's Name : PARAMJIT
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Industrial Training	-	44/50	44/50
02	Employability Skills - I	-	43/50	43/50
03	Electrical Machines-II (120951/30951)	48/100	40/50	88/150
04	Electrical Power-I (120952/30952/106552)	47/100	-	47/100
05	Industrial Electronics and Control of Drives (120953/30953/105853)	53/100	44/50	97/150
06	Digital Electronics and Microprocessors (120955/30955)	44/100	43/50	87/150
07	Minor Project Work	-	40/50	40/50
08	Environmental Education (120151)	65/100	-	65/100
09				
Internal Assessment				298/350
Grand Total				809/1150

Total(In Words) : EIGHT HUNDRED NINE

Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula

Principal

Dated: 21-01-2019

Deputy Secretary

Haryana State Board of Technical Education
Panchkula

Controller of Examination
Haryana State Board of Technical Education
Panchkula

Serial No. 19Y6011090007M06

Roll No. 160110900023

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA**DETAILED MARKS CERTIFICATE
3-YEAR / 6-SEMESTER DIPLOMA COURSE IN
ELECTRICAL ENGINEERING DIPLOMA EXAMINATION**

Name of the Candidate : GURPREET
 Father's Name : HARMESH LAL
 Mother's Name : PARAMJIT
 Year of Admission : 2016
 Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

6th Semester Detailed Marks Certificate				
Exam. held in : June 2019				
Sr. No.	Subjects	Theory	Practical	Internal Assessment
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Employability Skills - II	-	45/50	-
02	Utilization of Electrical Energy (120961/30961)	72/100	-	-
03	Electrical Power - II (120963/30963)	65/100	43/50	-
04	PLCs and Microcontrollers (120962/105864/031065A)	72/100	36/50	-
05	Energy Management/Optical Fiber Communication/Installation and Maintenance of Electrical equipments (120965A/B/C/030965A)	73/100	-	-
06	Entrepreneurship Development and Management (120264/30364)	59/100	-	-
07	Major Project Work	-	90/100	-
08				-
09				-
Total		341/500	214 /250	269 / 325
Grand Total				824/1075

Result: PASS

Total(In Words) : EIGHT HUNDRED TWENTY-FOUR

semester	I	II	III	IV	V	VI	Total	%	Division
Maximum Marks	1250	1300	1125	1100	1150	1075	7000	70.50	1st
Marks Obtained	898	879	798	727	809	824	4935		
Passed in (Month/Year)	DEC 2016	JUNE 2017	JUNE 2018	JUNE 2018	DEC 2018	JUNE 2019			

* Indicates that the candidate passed with grace marks.

Principal

Dated: 28-06-2019

Deputy Secretary
Haryana State Board of Technical Education
Panchkula

Controller of Examination
Haryana State Board of Technical Education
Panchkula

GOVERNMENT POLYTECHNIC, SIRSA-125055

PROVISIONAL CERTIFICATE

Certified that **Mr. / Ms. Gurpreet S/o / D/o Sh. Harmesh Lal** and **Smt. Paramjit** Roll No. **160110900023** was a student of this Institution from the session **2016 to 2019**. He/ She has passed the three years Diploma Course in **Electrical Engineering** conducted by the **State Board of Technical Education, Haryana** in **June-2019** Exam session.

He / She has secured **4935** Marks out of **7000** and has been placed in **1st** Division.

Semester Wise marks are as below:-

Semester	I	II	III	IV	V	VI	Total	Percentage	Division
Marks Obtained	898	879	798	727	809	824	4935	70.50	1st
Max. Marks	1250	1300	1125	1100	1150	1075	7000		
Passing Year	Dec-16	Jun-17	Jun-18	Jun-18	Dec-18	Jun-19	Jun-19		

As per institution record, he/she bears a good moral character.

Memo No. J-19/EE/21

Dated: 09-Jul-19


Exam Clerk


OMI Exam.


HOD


Principal
PRINCIPAL
Govt. Polytechnic SIRSA

THIS PROVISIONAL CERTIFICATE IS SUBJECT TO ALTERATION, MODIFICATION AND FULFILLMENT OF ELIGIBILITY CONDITION

S. No. 234834

Roll No. 160110900023



HARYANA STATE BOARD OF TECHNICAL EDUCATION DIPLOMA

Mr./Ms. GURPREET

Son/Daughter of Sh. HARMESH LAL

(Father)

and Smt. PARAMJIT

(Mother)

Having completed the prescribed THREE year Diploma

Course of instruction in ELECTRICAL ENGINEERING

at GOVT. POLYTECHNIC, SIRSA

and having passed the Final State Board Examination in JUNE - 2019

in the FIRST Division securing 4935 marks out of 7000

is hereby awarded the Diploma.

Place : Panchkula

Deputy Secretary

Controller of Exam.