S. No. Nº 0095295.



Regn./Roll No. 071509016

# DIPLOMA NATE BOARD OF TECHNICAL FOLICATION NATE BOARD OF TECHNICAL FOLICAL FOLIC

Sh. / Ms	GULAB SIN	GH COL			
Son / Daught	ter of Shri	ROHTASH	SINGH		(FATHER)
and Smt	SUN	IAN DEVI		1/	(MOTHER)
Having comp	leted the presc	ribed	THREE	7	year Diploma
Course of inst	truction in	ELECTRICA	L ENGINI	EERING	
atSE	ETH JAI PARK	ASH POLYTE	ECHNIC, I	DAMLA	and
Having passe	ed the Final S	tate Board Ex	caminatio	n in	May / June, 2011
in theSECO	DND Divis	ion securing _	3895	_marks	out of
is hereby awa	rded the Diplo	ma.			

Place: PANCHKULA

Serial No.: 10 Roll No.: 071509016

# Haryana State Board of Technical Education



### **DETAILED MARKS CERTIFICATE**

First Semester of Electrical Engineering Diploma Examination Held in Dec - 2010

Name of the Candidate

**GULAB SINGH** 

Father's Name

Sh. ROHTASH SINGH

Mother's Name

: Smt. SUMAN DEVI

Institute

Seth Jai Parkash Polytechnic, Damla (Yamuna Nagar)

Sr No.		Th	eory Sub	jects	Practic	al and Vi	va Voce
	Subjects	Max. Marks	Pass Marks	Marks Obtained	Max. Marks	Pass Marks	Marks Obtained
1	Communication Skills-I (030011)	100	35	36	50	20	44
2	Applied Maths-I (030012)	100	35	44			
3	Applied Physics-I (030013)	100	35	35	50	20	32
4	Applied Chemistry-I (030014)	100	35	43	50	20	38
5	Engineering Drawing-I (030015)	100	35	40	25	10	13
6	Basics of Information Technology (030016P)	THE PARTY		-	100	40	69
7	Workshop (030017P)				100	40	66
8	Internal Assessment (15IA1)	375	150	253			

Total

713 / 1250

In Words

Seven Hundred Thirteen

Result

Pass

\* Passed the subject with Grace Marks

\*\* Awarded Grace Marks for upgradation of division

Dated:

22-07-2011

Place: Panchkula

/ Controller of Exam Haryana State Board of Technical Education, Panchkula

# Haryana State Board of Technical Education



### **DETAILED MARKS CERTIFICATE**

Second Semester of Electrical Engineering Diploma Examination Held in May/Jun - 2011

Name of the Candidate : GULAB SINGH

Father's Name : Sh. ROHTASH SINGH

Mother's Name : Smt. SUMAN DEVI

Institute : Seth Jai Parkash Polytechnic, Damla (Yamuna Nagar)

Sr No.		Th	eory Sub	jects	Practic	Practical and Viva Voce		
	Subjects	Max. Marks	Pass Marks	Marks Obtained	Max. Marks	Pass Marks	Marks Obtained	
1	Communication Skills-II (030021)	100	35	47	50	20	38	
2	Applied Maths-II (030022)	100	35	46	-	h=1s		
3	Applied Physics-II (030023)	100	35	61	50	20	20	
4	Applied Chemistry-II (030024)	100	35	35*	50	20	25	
5	Engineering Drawing-II (030025)	100	35	39	25	10	10	
6	General Engineering (030827A)	100	35	35	50	20	20	
7	Workshop (030028P)	-	-		100	40	62	
8	Internal Assessment (15IA2)	375	150	179	-		-	

Total

617 / 1300

In Words

Six Hundred Seventeen

Result : Pass

Passed the subject with Grace Marks

\*\* Awarded Grace Marks for upgradation of division

Dated:

09-12-2011

Place: Panchkula

Controller of Exam

Haryana State Board of Technical

Education, Panchkula

Roll No.: 071509016

# Haryana State Board of Technical Education



### **DETAILED MARKS CERTIFICATE**

Third Semester of Electrical Engineering Diploma Examination Held in May/Jun - 2011

Name of the Candidate

**GULAB SINGH** 

Father's Name

: Sh. ROHTASH SINGH

Mother's Name

Smt. SUMAN DEVI

Institute

Seth Jai Parkash Polytechnic, Damla (Yamuna Nagar)

Sr No.		Th	eory Sub	ubjects Practical and Viva			va Voce
	Subjects	Max. Marks	Pass Marks	Marks Obtained	Max. Marks	Pass Marks	Marks Obtained
1	Electrical and Electronics Engineering Materials (030931)	100	35	35	200		-
2	Fundamentals of Electrical Engineering (030932)	100	35	51	50	20	36
3	Basic Electronics (030933)	100	35	38	50	20	23
4	Electrical Engineering Design and Drawing-I (030934)	100	35	35	50	20	20
5	Computer Programming and Applications (030935)	100	35	68	50	20	20
6	Communcation Skill-III (080037C)	100	35	40	50	20	27
7	Workshop (9WP3P)	-		-	50	20	35
8	Internal Assessment (15IA3)	425	170	186	-		-

Total

614 / 1325

In Words :

Six Hundred Fourteen

Result

Pass

\* Passed the subject with Grace Marks

\*\* Awarded Grace Marks for upgradation of division

Dated:

09-12-2011

Place: Panchkula

Controller of Exam
Haryana State Board of Technical
Education, Panchkula

Serial No.: 3 Roll No.: 071509016

# Haryana State Board of Technical Education



## DETAILED MARKS CERTIFICATE

Fourth Semester of Electrical Engineering Diploma Examination Held in Dec - 2010

Name of the Candidate

**GULAB SINGH** 

Father's Name

Sh. ROHTASH SINGH

Mother's Name

Smt. SUMAN DEVI

Institute

Seth Jai Parkash Polytechnic, Damla (Yamuna Nagar)

		Theory Subjects			Practical and Viva Voce			
Sr No.	Subjects		Pass Marks	Marks Obtained	Max. Marks	Pass Marks	Marks Obtained	
1	Electrical Machines-I (030941)	100	35	39	50	20	32	
2	Electrical Measurements and Measuring Instruments (030942)	100	35	39	50	20	33	
3	Electronic Devices and Circuits (030943)	100	35	49	50	20	30	
4	Electrical Engineering Design and Drawing-II (030944)	100	35	55	50	20	30	
5	Estimating and Costing in Electrical Engineering (030945)	100	35	40		-	-	
6	Communcation Skill (080047C)	100	35	35	50	20	34	
7	Workshop (030946P)	-			50	20	34	
8	Internal Assessment (15IA4)	425	170	282			-	

Total

732 / 1325

In Words

Seven Hundred Thirty Two

Result

Pass

\* Passed the subject with Grace Marks

\*\* Awarded Grace Marks for upgradation of division

Dated:

22-07-2011

Place: Panchkula

/ Controller of Exam Haryana State Board of Technical Education, Panchkula Additional S

Serial No.: 8

Roll No.: 071509016

# Haryana State Board of Technical Education



### **DETAILED MARKS CERTIFICATE**

Fifth Semester of Electrical Engineering Diploma Examination Held in May/Jun - 2011

Name of the Candidate

**GULAB SINGH** 

Father's Name

Sh. ROHTASH SINGH

Mother's Name

Smt. SUMAN DEVI

Institute

Seth Jai Parkash Polytechnic, Damla (Yamuna Nagar)

Sr No.		Th	Theory Subjects Practical and Viva V			a Voce	
	Subjects	Max. Marks	Pass Marks	Marks Obtained	Max. Marks	Pass Marks	Marks Obtained
1	Electrical Machines II (030951)	100	35	45	50	20	32
2	Power I (GT and DEP) (030952)	100	35	73		Indian	4
3	Industrial Electronics and Control of Drives (030953)	100	35	35	50	20	41
4	Elective I (Non Con. EE Sources) (030954B)	100	35	42		-	-
5	Digital Electronics and Microprocessors (030955)	100	35	78	50	20	40
6	Minor Project Work (9MPW5)	-		- /	100	40	77
7	Internal Assessment (15IA5)	325	130	244	-	-	-

Total

707 / 1075

In Words :

Seven Hundred Seven

Result

Pass

\* Passed the subject with Grace Marks

\*\* Awarded Grace Marks for upgradation of division

Dated:

09-12-2011

Place: Panchkula

Controller of Exam
Haryana State Board of Technical
Education, Panchkula

# Kurukshetra University

(Established by the State Legislature Act XII of 1956)

Serial No. 679



Roll No. 73103/6
Regn. No. // 9904 415

RESULT-cur-DETAILED MARKS CARD

Bachelor of Technology 6th Semester (Electrical Engineering)

Examination May/Pec......2003.

me: Gulas

Father's Name: Shri Roh tosh 8 mgg

### DETAIL OF MARKS

		Theory	Practicals	Sessionals			
Sr. No.	PAPERS	Marks Obtained	Min.Pass/ Max.Marks	Marks Obtained	Maximum Marks		
1.	Power System Analysis & Protection	40	40/100	ш	50		
2.	Microcontrollers & Applications	58	40/100	40	50		
3.	Digital Signal Processing	46	40/100	40	50		
4.	Power Electronics-II	52	40/100	40	50		
5.	Electric Drives & Traction	40	40/100	41	50		
6.	Advance Programming	62	40/100	41	50		
7.	Advance Programming Lab.	19	10/25	36	50		
8.	Microcontrollers Application Lab.	22	10/25	38	50		
9.	Power Electronics-II Lab.	19	10/25	18	25		
10.	Electric Drives Lab.	20	10/25	19	25		

Note:—"Indicates that the candidate has been granted grace marks for passing in this/these subject(s).

735

out of 1150

RE	SULT : See Sr. No below	
4	Passed and has obtainted	+
5.	He/She is required to re-appear if otherwise eligible under the Ordinances in	

20614

Grand Total