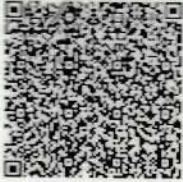


HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE
1st Year of ELECTRONICS AND COMMUNICATION ENGINEERING Diploma Examination
Held In July 2020

Name of the Candidate : GURKIRAT SINGH
Father's Name : GURPAL SINGH
Mother's Name : JASMEET KAUR
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

Sr. No.	Subjects	Theory		Practical	Total
		1 st & 2nd Assessment Obtained Marks/ Max Marks	Final Assessment Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	English (180011)	20/40	30/60	42/50	92/150
02	Applied Mathematics (180012)	40/40	60/60	-	100/100
03	Applied Physics (180013)	33/40	50/60	42/50	125/150
04	Computer Fundamentals (180814)	26/40	39/60	-	65/100
05	Fundamental of Electrical and Electronics Engineering (180817)	27/40	41/60	42/50	110/150
06	Environmental Studies (180016)	31/40	47/60	46/50	124/150
07	Engineering Graphics (180015)	16/40	24/60	20/25	60/125
08	Information Technology (180018)	-	-	42/50	42/50
09	Internet of Things & Artificial Intelligence (180019)	-	-	90/100	90/100
10	General Workshop Practice (180020)	-	-	41/50	41/50
Internal Assessment					256/300
Grand Total		193/280	291/420	365/425	1105/1425

Total(In Words) : ONE THOUSAND ONE HUNDRED FIVE

Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE

3rd Semester of ELECTRONICS AND COMMUNICATION ENGINEERING Diploma Examination
Held In January 2021

Name of the Candidate : GURKIRAT SINGH
Father's Name : GURPAL SINGH
Mother's Name : JASMEET KAUR
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Electronic Instruments and Measurement (181036)	62/100	45/50	107/150
02	Principles of Communication Engineering (181033)	54/100	42/50	96/150
03	Digital Electronics (180832)	68/100	45/50	113/150
04	Electronic Devices and Circuits (181031)	61/100	45/50	106/150
05	Network Filters and Transmission lines (181035)	59/100	45/50	104/150
06	Computer Programming Using C (181034)	60/100	45/50	105/150
07				
08				
09				
Internal Assessment				289/325
Grand Total				920/1225

Total(In Words) : NINE HUNDRED TWENTY

Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE

**4th Semester of ELECTRONICS AND COMMUNICATION ENGINEERING Diploma Examination
Held In August 2021**

Name of the Candidate : GURKIRAT SINGH
Father's Name : GURPAL SINGH
Mother's Name : JASMEET KAUR
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA(011)

Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Instrumentation (181041)	76/100	42/50	118/150
02	Communication System (181042)	75/100	42/50	117/150
03	Power Electronics (181043)	74/100	36/50	110/150
04	Microprocessor and Peripheral Devices (180844)	74/100	36/50	110/150
05	Troubleshooting of Electronics Equipment (181046)	-	42/50	42/50
06	Medical Electronics (181045)	76/100	43/50	119/150
07				
08				
09				
Internal Assessment				272/325
Grand Total				888/1125

Total(In Words) : EIGHT HUNDRED EIGHTY-EIGHT

Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula

Rajendra Jindal

Gurkirat Singh

Gurpal Singh

HARYANA STATE BOARD OF TECHNICAL EDUCATION PANCHKULA



DETAILED MARKS CERTIFICATE

5th Semester of ELECTRONICS AND COMMUNICATION ENGINEERING Diploma Examination
Held In February 2022

Name of the Candidate : GURKIRAT SINGH
 Father's Name : GURPAL SINGH
 Mother's Name : JASMEET KAUR
 Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA (011)

Sr. No.	Subjects	Theory	Practical	Total
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Industrial Training	-	89/100	89/100
02	Audio Video Systems (181052)	43/100	43/50	86/150
03	Digital Communication (181053)	73/100	43/50	116/150
04	Optical Fiber Communication (181054)	65/100	44/50	109/150
05	Computer Networks (181051)	57/100	-	57/100
06	Microcontrollers (181055)	74/100	44/50	118/150
07	Electronics Design and Simulation Techniques	-	37/50	37/50
08				
09				
Internal Assessment				354/400
Grand Total				966/1250

Total(In Words) : NINE HUNDRED SIXTY-SIX

Result: PASS

* Indicates that the candidate passed with grace marks.

Place: Panchkula

Panchkula

Gurkirat Singh

Gurkirat Singh



DETAILED MARKS CERTIFICATE
3-YEAR / 6-SEMESTER DIPLOMA COURSE IN
ELECTRONICS AND COMMUNICATION ENGINEERING DIPLOMA EXAMINATION

Name of the Candidate : GURKIRAT SINGH
Father's Name : GURPAL SINGH
Mother's Name : JASMEET KAUR
Year of Admission : 2019
Name of the Institute/Code : GOVT. POLYTECHNIC, SIRSA (011)

6th Semester Detailed Marks Certificate				
Exam. held in : July 2022				
Sr. No.	Subjects	Theory	Practical	Internal Assessment
		Obtained Marks/ Max Marks	Obtained Marks/ Max Marks	Obtained Marks/ Max Marks
01	Microwave and Radar Engineering (181061)	60/100	45/50	-
02	Wireless and Mobile Communication (181063)	63/100	45/50	-
03	Entrepreneurship Development and Management (180264)	42/100	-	-
04	Industrial Automation/Embedded System (181062B/C)	55/100	-	-
05	Project Work	-	90/100	-
06				-
07				-
08				-
09				-
	Total	220/400	180 /200	189 / 225
	Grand Total			589/825

Result: PASS

Total(In Words) : FIVE HUNDRED EIGHTY-NINE

Year/semester	I Year	III	IV	V	VI	Total	%	Division
Maximum Marks	1425	1225	1125	1250	825	5850	76.38	First Class With Distinction
Marks Obtained	1105	920	888	966	589	4468		
Passed in (Month/Year)	JULY 2020	JAN 2021	AUG 2021	FEB 2022	JULY 2022			

* Indicates that the candidate passed with grace marks.

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