

ਆਈ. ਕੇ. ਗੁਜਰਾਲ ਪੰਜਾਬ ਟੈਕਨੀਕਲ ਯੂਨੀਵਰਸਿਟੀ
J.K. Gujral Punjab Technical University

Formerly

S.No. of EDP

16/ 605556

Punjab Technical University

103556304

Regn. cum Roll No: **1311105**

Name of the College/Institute:

**Guru Nanak Dev Engineering
 College, Ludhiana**

(Punjab Govt. Aided Status) An Autonomous
 College Under UGC Act-1956 [2(f) and 12(B)]



Result cum Detailed Marks Card

Bachelor of Technology (Computer Science Engineering), FIRST Semester, Nov-2015

Name : **PARNEET KAUR**
 Father's Name : **MANVINDER SINGH**
 Mother's Name : **JATINDER KAUR**

Subject Code	Subject	Type	Marks Obtd.	Min.Pass Marks	Max. Marks
BTAM-101	Engineering Mathematics-I	Theory	37	24	60
		Sessional	19	16	40
BTEE-101	Basic Electrical and Electronics Engineering	Theory	24	24	60
		Sessional	20	16	40
BTEE-102	Basic Electrical and Electronics Engineering Laboratory	Practical	14	8	20
		Sessional	20	12	30
BTHU-101	Communicative English	Theory	26	24	60
		Sessional	26	16	40
BTHU-102	Communicative English Laboratory	Practical	15	8	20
		Sessional	22	12	30
BTMP-101	Manufacturing Practice	Practical	32	16	40
		Sessional	51	24	60
BTPH-101	Engineering Physics	Theory	24	24	60
		Sessional	20	16	40
BTPH-102	Engineering Physics Laboratory	Practical	14	8	20
		Sessional	23	12	30
HVPE-101	Human Values and Professional Ethics	Theory	28	24	60
		Sessional	28	16	40
TOTAL (If Pass)			443		750

Result Status : PASS

Total Marks Obtained (In Words) : FOUR FOUR THREE ONLY

Notification Result Date : 22.04.2016

Date of Issue : 18.11.2016

Place : JALANDHAR

E.D.P. CELL

Prepared By

Checked & Verified By

Officer Incharge

Controller of Examinations

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J.K. Gujral Punjab Technical University

Formerly

S.No. of EDP

16/ **605557** Punjab Technical University

103814833

Regn. cum Roll No: **1311105**

Name of the College/Institute:

**Guru Nanak Dev Engineering
 College, Ludhiana**

(Punjab Govt. Aided Status) An Autonomous
 College Under UGC Act-1956 [2(f) and 12(B)]



Result cum Detailed Marks Card

Bachelor of Technology (Computer Science Engineering), SECOND Semester, Apr-2016

Name : **PARNEET KAUR**
 Father's Name : **MANVINDER SINGH**
 Mother's Name : **JATINDER KAUR**

Subject Code	Subject	Type	Marks Obtd.	Min.Pass Marks	Max. Marks
BTAM-102	Engineering Mathematics-II	Theory	28	24	60
		Sessional	16	16	40
BTCH-101	Engineering Chemistry	Theory	29	24	60
		Sessional	24	16	40
BTCH-102	Engineering Chemistry-Lab	Practical	12	8	20
		Sessional	18	12	30
BTCS-101	Fundamentals of Computer Programming and IT	Theory	25	24	60
		Sessional	23	16	40
BTCS-102	Fundamentals of Computer Programming and IT-Lab	Practical	12	8	20
		Sessional	22	12	30
BTME-101	Elements of Mechanical Engineering	Theory	24	24	60
		Sessional	24	16	40
BTME-102	Engineering Drawing	Theory	26	24	60
		Sessional	20	16	40
BTME-103	Engineering Computer Graphics-Lab	Practical	14	8	20
		Sessional	26	12	30
EVSC-101	Environmental Science	Theory	30	24	60
		Sessional	24	16	40
GF2	General Fitness				
		Sessional	86	40	100
TOTAL (If Pass)			483		850

Result Status : PASS

Total Marks Obtained (In Words) : FOUR EIGHT THREE ONLY

Notification Result Date : 22.07.2016

Date of Issue : 18.11.2016

Place : JALANDHAR

E.D.P. CELL

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Controller of Examinations

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Punjab Technical University

S.No. of EDP

102868869

Regn. cum Roll No: **1311105**

Name of the College/Institute:

**Guru Nanak Dev Engineering
College, Ludhiana**
**(Punjab Govt. Aided Status) An Autonomous
College Under UGC Act-1956 [2(f) and 12(B)]**


Result cum Detailed Marks Card

Bachelor of Technology (Computer Science Engineering), THIRD Semester, Nov-2014

Name : **PARNEET KAUR**
 Father's Name : **MANVINDER SINGH**
 Mother's Name : **JATINDER KAUR**

Subject Code	Subject	Type	Marks Obtd.	Min.Pass Marks	Max. Marks	
BTAM-302	Mathematics-III	Theory	28	24	60	100
		Sessional	26	16	40	
BTCS-301	Computer Architecture	Theory	26	24	60	100
		Sessional	26	16	40	
BTCS-303	Digital Circuits & Logic Design	Theory	26	24	60	100
		Sessional	23	16	40	
BTCS-304	Data Structures	Theory	24	24	60	100
		Sessional	22	16	40	
BTCS-305	Object Oriented Programming using C++	Theory	43	24	60	100
		Sessional	18	16	40	
BTCS-306	Data Structures Lab	Practical	15	8	20	50
		Sessional	22	12	30	
BTCS-307	Institutional Practical training	Practical	33	16	40	100
		Sessional	44	24	60	
BTCS-308	Digital Circuits & Logic Design Lab	Practical	12	8	20	50
		Sessional	22	12	30	
BTCS-309	Object Oriented Programming Using C++	Practical	13	8	20	50
		Sessional	22	12	30	
TOTAL (If Pass)			445		750	

Result Status : PASS
Total Marks Obtained (In Words) : FOUR FOUR FIVE ONLY
Notification Result Date : 21.05.2015
Date of Issue : 18.11.2016
Place : JALANDHAR
E.D.P. CELL

Prepared By

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Controller of Examinations

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341999

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Punjab Technical University

S.No. of EDP
103220900

Regn. cum Roll No: 1311105

Name of the College/Institute:

**Guru Nanak Dev Engineering
College, Ludhiana**
(Punjab Govt. Aided Status) An Autonomous
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Result cum Detailed Marks Card

Bachelor of Technology (Computer Science Engineering), FOURTH Semester, April-2015

Name : **PARNEET KAUR**
Father's Name : **MANVINDER SINGH**
Mother's Name : **JATINDER KAUR**

Subject Code	Subject	Type	Marks Obtd.	Min.Pass Marks	Max. Marks
BTCS-407	Computer Networks-I Lab	Practical	14	8	20
		Sessional	25	12	30
BTCS401	Operating Systems	Theory	24	24	60
		Sessional	23	16	40
BTCS402	Discrete Structures	Theory	26	24	60
		Sessional	24	16	40
BTCS403	Computer Networks-I	Theory	27	24	60
		Sessional	17	16	40
BTCS404	Microprocessor and Assembly Language Programming	Theory	30	24	60
		Sessional	26	16	40
BTCS405	System Programming	Theory	24	24	60
		Sessional	21	16	40
BTCS406	Operating System Lab	Practical	12	8	20
		Sessional	18	12	30
BTCS408	Microprocessor & Assembly Language Programming Lab	Practical	14	8	20
		Sessional	21	12	30
BTCS409	System Programming Lab	Practical	16	8	20
		Sessional	25	12	30
GF5	General Fitness				100
		Sessional	83	40	100
TOTAL (If Pass)			470		800

Result Status : **PASS**Total Marks Obtained (In Words) : **FOUR SEVEN ZERO ONLY**

Notification Result Date : 02.09.2015

Date of Issue : 18.11.2016

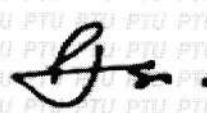
Place : **JALANDHAR**

E.D.P. CELL

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Controller of Examinations

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J.K. Gujral Punjab Technical University

Formerly

S.No. of EDP

103812613

16/ **605558** Punjab Technical University

Regn. cum Roll No: 1311105

Name of the College/Institute:

**Guru Nanak Dev Engineering
 College, Ludhiana**

(Punjab Govt. Aided Status) An Autonomous
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Result cum Detailed Marks Card

Bachelor of Technology (Computer Science Engineering), SIXTH Semester, Apr-2016

Name : **PARNEET KAUR**
 Father's Name : **MANVINDER SINGH**
 Mother's Name : **JATINDER KAUR**

Subject Code	Subject	Type	Marks Obtd.	Min. Pass Marks	Max. Marks
BTCS-601	Simulation and Modeling	Theory	33	24	60
		Sessional	24	16	40
BTCS-602	RDBMS-II	Theory	24	24	60
		Sessional	20	16	40
BTCS-603	Software Engineering	Theory	28	24	60
		Sessional	22	16	40
BTCS-604	RDBMS-II Lab	Practical	15	8	20
		Sessional	18	12	30
BTCS-605	Free/ Open Source Software Lab	Practical	15	8	20
		Sessional	25	12	30
BTCS-606	Software Engineering Lab	Practical	17	8	20
		Sessional	20	12	30
BTCS-607	Simulation and Modeling Lab	Practical	13	8	20
		Sessional	20	12	30
BTCS-904	Information Security	Theory	37	24	60
		Sessional	24	16	40
GF	General Fitness				100
		Sessional	87	40	100
HU-251	Human Resource Management	Theory	33	24	60
		Sessional	20	16	40
TOTAL (If Pass)			495		800

Result Status : PASS

Total Marks Obtained (In Words) : FOUR NINE FIVE ONLY

Notification Result Date : 22.07.2016

Date of Issue : 18.11.2016

Place : JALANDHAR

E.D.P. CELL.

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